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APPLICANTS Chao-Chiang Chen, Cupertino, CA; J. George Janac, Cupertino, CA; *** CONTINUING DATA **********************************							
Foreign Priority claimed 35 USC 119 (a-d) condition met Verified and Acknowledged ADDRESS	STATE OR COUNTRY CA	SHEETS TOT DRAWING CLAI 17 2			INDEPENDENT CLAIMS 1		
RONALD CRAIG FISH P.O. BOX 820 LOS GATOS, CA95031							
TITLE Method of optimizing IC logic performance by static timing based parasitic budgeting							
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